

UIU students working at



Tasnuva



Protik



Masud



Rifat



Khaled

Coming Attractions

1. Digital Logic Design Using Verilog
2. Mixed Signal IC Design
3. PLL and Clock Circuits
4. Developing the Nanometer ASIC: From Spec to Silicon

Schedule, Cost & Qualification

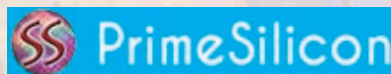
1. Classes will be held on Saturdays for 3 hours
2. Qualification for admission into this program:
 - B.Sc. (or final year of Under Graduate) in any Engineering or Science discipline
 - Diploma in Engineering or Equivalent
3. Admission form is available from the Admission Office @ UIU on payment of Tk. 500/=. Term fees are payable in Bank Draft/Pay Order.

4. Tuition fee for each course:

Category	Fees (Tk.)
UIU Student/Alumnus	5000/=
External Student/Professional	5500/=

5. Group discount of 20% for admission of at least 5 students.

UIU students working at



Kulsum



Prosanto



Shahadat



Fattah



Didarul



Mishkat

VLSI Training Academy (VTA), UIU

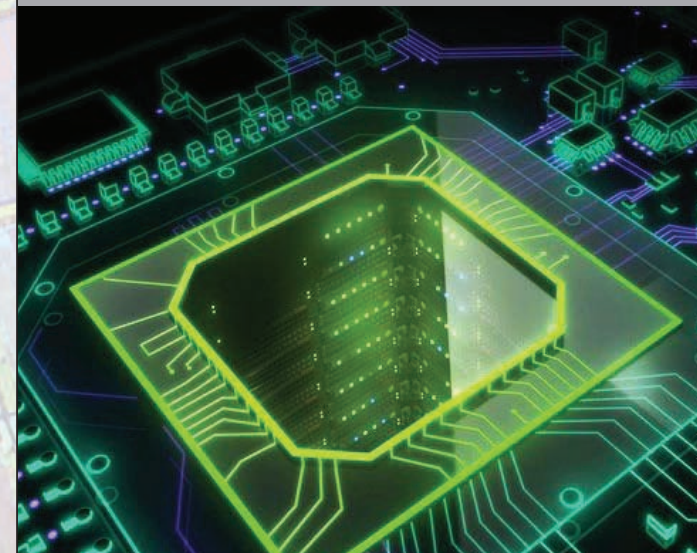
Earn as you Learn

Earn as you Learn

Earn as you Learn

VLSI Certificate Program

Professional Version of Cadence Tools



Please Contact:

Cell No: 01732-580412, 01711437332

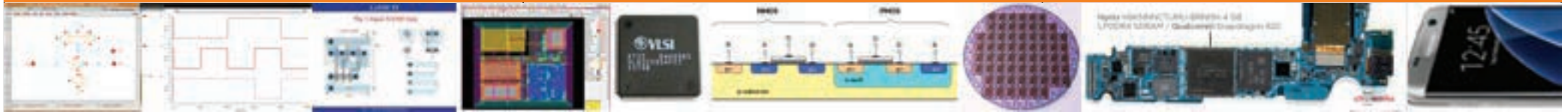
E-mail: sahmed@eee.uiu.ac.bd, ibchy@eee.uiu.ac.bd

United International University



**UNITED
INTERNATIONAL
UNIVERSITY**





VLSI Training Academy (VTA) @ UIU offers VLSI Certificate Program for students and professionals working in the IC design, ASIC, semiconductor device fields. Our VLSI training curriculum includes the most complete IC design courses of any training school in the area. Students gain practical experience using the latest EDA tools on Linux in our state-of-the-art VLSI lab.

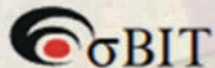
Earn As You Learn

Students are encouraged to try out real world test cases from outsourcing sites like UpWork to practice and sharpen their VLSI design skills. Professional skills required for winning outsourcing design jobs and completing them will be provided in this training.

Online Job Opportunities

www.upwork.com
www.freelancer.com
www.guru.com

UIU students working at



Omar



Ahsan

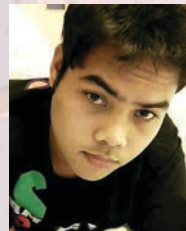
Benefits of studying VLSI @ VTA

- Use Cadence and update your knowledge of best design practices
- Interactions with expert instructors
- Practice with real EDA tools used by professionals in the field
- Access to high end Linux server with ample memory

UIU students working at



BJIT GROUP



Ashiq



Saiful

Instructors

Mr. Salahuddin Ahmed

8 Years Industry Experience at Qualcomm Inc., San Diego, USA

Dr. Md. Iqbal Bahar Chowdhury

35+ Analog Projects using Cadence

Mr. Md. Zubair Alam

HSPICE, Verilog-A, Spectre, Stanford CNFET Device Modeling

Offered Courses

1. Logic Synthesis (8 week/8 class)

Outlines various concepts of logic synthesis, starting with the basics of synthesis using RTL Compiler from Cadence. Covers various options such as design partitioning, optimization, time / area constraints and library management. Intended for design engineers with basic knowledge of Verilog/VHDL.

2. Physical Design (8 week/8 class)

Place and Route flow from netlist to GDSII with Cadence Encounter. Starts with partitioning, floor plan, and power plan; followed by placement/optimization, CTS and routing; completes with extraction, STA and physical verification.

3. Analog Circuit Design (8 week/8 class)

Analog circuit design flow with Cadence Virtuoso. Process starts with modeling, schematic design, simulation and sizing followed by layout, LVS, DRC, RC extraction and delay calculation. Op-amp and amplifiers will be used to learn the flow followed by practical design from UpWork

4. Script Writing (8 week/8 class)

From basics to advanced scripting in Perl and/or Tcl/Tk. Covers automation of different steps of ASIC design flow from revision control to report generation to seamless communication between different tools. Intended for engineers with basic knowledge of Linux and some programming skills.